In the Claims

This listing of claims will replace all prior versions and listings of claims in the application:

1. (Currently Amended) A method of exporting from a data processor emulation information including emulation control information and emulation data, comprising:

arranging the emulation information into fixed length information blocks;

outputting a sequence of the information blocks from the data processor via a plurality of terminals of the data processor; ~~and~~

said arranging step including providing some of the information blocks of the sequence with relative proportions of emulation control information and emulation data that differ from the relative proportions of emulation control information and emulation data in other blocks of the sequence;

storing comparison data;

comparing respective sections of emulation data with the stored comparison data; and

wherein the emulation control information in one of the information blocks includes a compression map indicative of whether the sections of the emulation data match the stored comparison data.

2 to 4. (Canceled)

5. (Currently Amended) ~~The~~ A method of ~~Claim 1, wherein~~ exporting from a data processor emulation information including emulation control information and emulation data, comprising:

arranging the emulation information into fixed length information blocks, the emulation data in one of the information

blocks includes bits indicating whether the data processor performed data processing operations during a corresponding clock cycle;

outputting a sequence of the information blocks from the data processor via a plurality of terminals of the data processor; and

said arranging step including providing some of the information blocks of the sequence with relative proportions of emulation control information and emulation data that differ from the relative proportions of emulation control information and emulation data in other blocks of the sequence.

6 to 12. (Canceled)

13. (Currently Amended) An integrated circuit device, comprising:

a data processing portion for performing data processing operations;

an emulation information collector coupled to said data processing portion for receiving emulation data therefrom, said collector operable for arranging the emulation data and associated emulation control information into fixed length information blocks;

a plurality of terminals coupled to said collector for permitting said collector to communicate with an emulation controller located externally of said integrated circuit device; ~~and~~

said collector operable for providing to said terminals a sequence of said information blocks to be output to the emulation controller, said collector further operable for providing some of the information blocks of the sequence with relative proportions of emulation control information and emulation data that differ from

the relative proportions of emulation control information and emulation data in other blocks of the sequence;

a comparison data register storing comparison data;

a comparitor connected to said comparison data register and receiving emulation data generating an indication of a match between corresponding sections of said comparison data and said emulation data; and

wherein the emulation control information in one of the information blocks includes a compression map indicative of whether the sections of the emulation data match the stored comparison data.

14 to 16. (Canceled)

17. (Currently Amended) ~~The~~ An integrated circuit device of ~~Claim 13, wherein~~ comprising:

a data processing portion for performing data processing operations;

an emulation information collector coupled to said data processing portion for receiving emulation data therefrom, said collector operable for arranging the emulation data and associated emulation control information into fixed length information blocks, the emulation data in one of the information blocks includes bits indicating whether the data processor performed data processing operations during a corresponding clock cycle;

a plurality of terminals coupled to said collector for permitting said collector to communicate with an emulation controller located externally of said integrated circuit device; and

said collector operable for providing to said terminals a sequence of said information blocks to be output to the emulation controller, said collector further operable for providing some of

the information blocks of the sequence with relative proportions of emulation control information and emulation data that differ from the relative proportions of emulation control information and emulation data in other blocks of the sequence.

18 to 27. (Canceled)